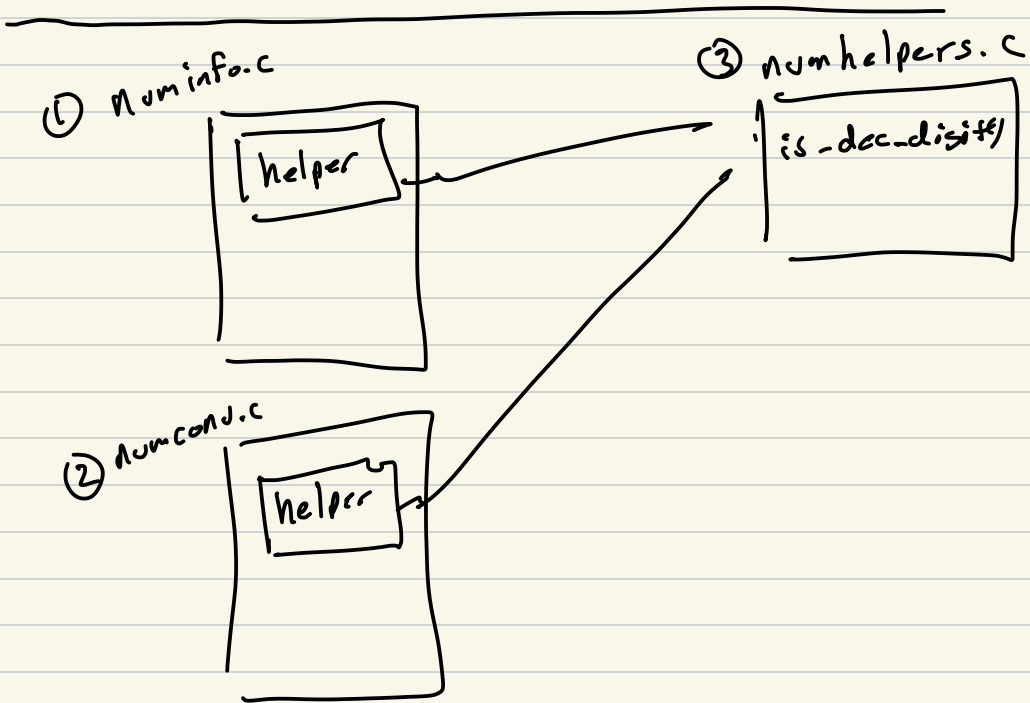


# CS 315-01 RISC-V Assembly Part 1

## Makefile

numinfo

numconv



Create a header numhelpers.h ← prototype

```
bool is_dec_digit(char c);  
:  
:
```

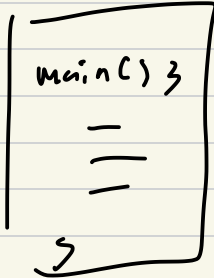
At the top of numinfo.c and numconv.c  
#include <stdio.h>  
:  
#include "numhelpers.h"

gcc -o numinfo numinfo.c numhelpers.c

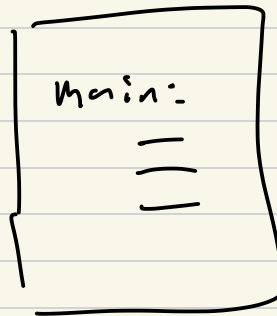
gcc -o numconv numconv.c numhelpers.c

---

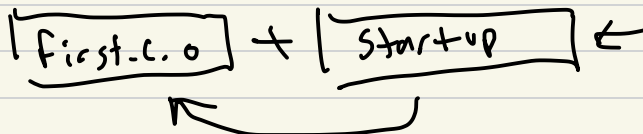
first.c.c



first.s.s

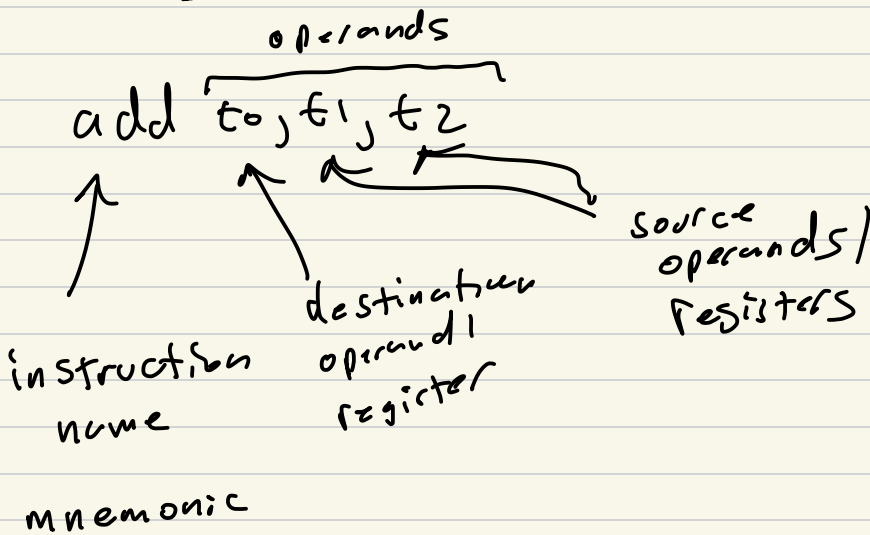


gcc -o first.o first.c.c



# RISC-V Assembly Language

Human Readable form of  
machine code / machine language



## Registers

RISC-V 64 bit

32 registers where each register holds a 64 bit value

$x_0, x_1, \dots, x_{31}$

$a_0, a_1, \dots \quad t_0, t_1, \dots$

